

**Preliminary Amendment**

Applicant: Werner Hemmert, et al.

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Title: **CIRCUIT ARRANGEMENT AND SIGNAL-PROCESSING DEVICE**

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**IN THE ABSTRACT**

Please add the following Abstract on the last page as follows:

**CIRCUIT ARRANGEMENT AND SIGNAL-PROCESSING DEVICE****Abstract**

A circuit arrangement having a filter bank with a plurality of filter stages and a filter-bank input to which an input signal can be supplied is disclosed. In one embodiment, the circuit arrangement has a plurality of resonator circuits for generation of in each case one output signal element from the input signal, with each resonator circuit in each case being associated with at least one filter stage from the plurality of filter stages, and being coupled to an output of the respective filter stage. Each resonator circuit has a capacitance, an inductance and a resonator output, at which the respective output signal element can be produced. Furthermore, at least one resonator control circuit for open-loop or closed-loop control of the Q-factor of at least one resonator circuit is provided in the circuit arrangement, with the resonator control circuit being designed in such a manner that it provides open-loop or closed-loop control for the Q-factor of the at least one resonator circuit as a function of the time profile of the signal amplitude of the input signal and/or of the output signal element from the at least one resonator circuit.